

Impedance-Matched IQ Network for an Image Rejection Circuit

Abstract of Disclosure

[0034] An impedance-matched IQ network includes a phase shift circuit and a back termination. The phase shift circuit includes an in-phase mixer port for receiving the in-phase signal, a quadrature-phase mixer port for receiving the quadrature phase signal, a termination port, and an output port. The back termination is coupled to the termination port of the phase shift circuit, the back termination having an impedance value substantially equal to the characteristic impedance of the phase shift circuit at the termination port.